Programming Guideline for S7-1200/1500
STEP 7 (TIA Portal)

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1 Preface

Aims for the development of the new SIMATIC control generation

- An engineering framework for all automation components (controller, HMI, drives, etc.)
- Uniform programming
- Increased performance
- Full set of commands for every language
- Fully symbolic program generation
- Data handling even without pointer
- Reusability of created blocks

Aim of the guideline

The new control generation SIMATIC S7-1200 and S7-1500 has an up-to-date system architecture, and together with the TIA Portal offers new and efficient options of programming and configuration. It is no longer the resources of the controller (e.g. data storage in the memory) that are paramount but the actual automation solution.

This document gives you many recommendations and tips on the optimal programming of S7-1200/1500 controllers. Some differences in the system architecture of the S7-300/400, as well as the thus connected new programming options are explained in an easy to understand way. This helps you to create a standardized and optimal programming of your automation solutions.

The examples described can be universally used for the controllers S7-1200 and S7-1500.

Core content of this programming guideline

The following key issues on the TIA Portal are dealt with in this document:

- S7-1200/1500 innovations
  - Programming languages
  - Optimized blocks
- Recommendation on general programming
  - Operating system and user program
  - Storage concept
  - Symbolic addressing
  - Libraries
- Recommendations on hardware-independent programming

Advantages and benefits

Numerous advantages arise by applying these recommendations and tips:

- Powerful user program
- Clear program structures
- Intuitive and effective programming solutions
2 S7-1200/1500 Innovations

2.1 Introduction

In general, the programming of SIMATIC controllers has stayed the same from S7-300/400 to S7-1500. There are the familiar programming languages such as LAD, FBD, STL, SCL or graph and blocks such as organization blocks (OBs), function blocks (FBs), functions (FCs) or data blocks (DBs). I.e. already created S7-300/400 programs can be implemented on S7-1500 and already created LAD, FBD and SCL programs on S7-1200 controller without any problems.

Additionally, there are many innovations that make programming easier for you and which allow a powerful and storage-saving code.

We not only recommend implementing programs that are implemented for S7-1200/1500 controllers 1:1 but also to check them for the new options and where applicable, to use them. The additional effort is often limited and you get a program code that is, for example,

- optimal in terms of memory and runtime for the newer CPUs
- easier to understand,
- and easier to maintain.

2.2 Terms

General terms using TIA Portal

Some terms have changed in order to make better handling with the TIA Portal possible.

Figure 2-1: New terms in the TIA Portal

<table>
<thead>
<tr>
<th>STEP 7 V5.x</th>
<th>STEP 7 (TIA Portal)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Symbol table</td>
<td>PLC tags</td>
</tr>
<tr>
<td>UDT</td>
<td>PLC data types</td>
</tr>
<tr>
<td>VAT table</td>
<td>Watch table</td>
</tr>
</tbody>
</table>

Terms for tags and parameters

When it is about tags, functions, and function blocks, many terms are repeatedly used differently or even incorrectly. The following figure is to clarify these terms.
### Figure 2-2: Terms associated with tags and parameters

#### Global DB

1. **Tag**
   - Tags are reserved memory areas for values in the controller. Tags are always defined with a certain data type (Bool, Integer, etc.):
   - PLC tags
   - Single tags in data blocks
   - Complete data blocks

2. **Tag value**
   - Tag values are values stored in a tag (e.g., 15 as value of an Integer tag).

3. **Actual parameter**
   - Actual parameters are tags interconnected at the interfaces of instructions, functions, and function blocks.

4. **Formal parameter**
   - Formal parameters are the interface parameters of instructions, functions, and function blocks (Input, Output, InOut, Temp, Static, and Return).

### Table 2-1: Terms associated with tags and parameters

<table>
<thead>
<tr>
<th>Term</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1. Tag</td>
<td>Tags are reserved memory areas for values in the controller. Tags are always defined with a certain data type (Bool, Integer, etc.):</td>
</tr>
<tr>
<td></td>
<td>• PLC tags</td>
</tr>
<tr>
<td></td>
<td>• Single tags in data blocks</td>
</tr>
<tr>
<td></td>
<td>• Complete data blocks</td>
</tr>
<tr>
<td>2. Tag value</td>
<td>Tag values are values stored in a tag (e.g., 15 as value of an Integer tag).</td>
</tr>
<tr>
<td>3. Actual parameter</td>
<td>Actual parameters are tags interconnected at the interfaces of instructions, functions, and function blocks.</td>
</tr>
<tr>
<td>4. Formal parameter (transfer parameter, block parameter)</td>
<td>Formal parameters are the interface parameters of instructions, functions, and function blocks (Input, Output, InOut, Temp, Static, and Return).</td>
</tr>
</tbody>
</table>

### Note

You will find further information in the following entries:

- What entries are available on the internet for the migration to STEP 7 (TIA Portal) and WinCC (TIA Portal)?

- What prerequisites have to be fulfilled in order to migrate a STEP 7 V5.x project into STEP 7 Professional (TIA Portal)?

- PLC migration for S7-1500 with STEP 7 (TIA Portal) V12

- Programming recommendations for S7-1200 and S7-1500 with STEP 7 (TIA Portal) V12

- Why is it not possible to mix register passing and explicit parameter transfer with the S7-1500 in STEP 7 (TIA Portal) V12?
  - Among others, the migration of STL programs to S7-1500 is described in this entry.
2.3 Programming languages

For the programming of a user program, various different programming languages are available. Each language has its own advantages, which can be variably used, depending on the application. Every block in the user program can therefore be created in any programming language.

Table 2-2: Programming languages

<table>
<thead>
<tr>
<th>Programming language</th>
<th>S7-1200</th>
<th>S7-1500</th>
</tr>
</thead>
<tbody>
<tr>
<td>Ladder (LAD)</td>
<td>✓</td>
<td>✓</td>
</tr>
<tr>
<td>Function block diagram (FBD)</td>
<td>✓</td>
<td>✓</td>
</tr>
<tr>
<td>Structured control language (SCL)</td>
<td>✓</td>
<td>✓</td>
</tr>
<tr>
<td>Graph</td>
<td>planned</td>
<td>✓</td>
</tr>
<tr>
<td>Statement list (STL)</td>
<td>x</td>
<td>✓</td>
</tr>
</tbody>
</table>

Note

You will find further information in the following entries:

SIMATIC S7-1200 / S7-1500 Comparison list for programming languages

What has to be observed when migrating a S7-SCL program in STEP 7 (TIA Portal)?

What instructions cannot be used in STEP 7 V11 in an SCL program?

How can the constants be defined under STEP 7 V11 in a S7-SCL program?

2.4 Optimized machine code

TIA Portal and S7-1200/1500 allow an optimized runtime performance in any programming language. All languages are compiled the same, directly into the machine code.

Advantages

- All programming languages have the same high performance (with the same access types)
- No reduced performance through additional compiling with an intermediate step via STL

Properties

The following figure displays the difference of the compilation of S7 programs into machine code.
Figure 2-3: Machine code generation with S7-300/400/WinAC and S7-1200/1500

- For S7-300/400/WinAC controllers LAD and FBD programs are first of all compiled in STL before the machine code is created.
- For S7-1200/1500 controllers all programming languages are directly compiled into machine code.

2.5 Block creation

All blocks such as OBs, FBs and FCs can be programmed directly in the desired programming language. Thus no source has to be created for SCL programming. You only select the block, and SCL as programming language. The block can then be directly programmed.

Figure 2-4: "Add new block" dialog
2.6 Optimized blocks

S7-1200/1500 controllers have optimized data storage. In optimized blocks, all tags are automatically sorted by their data type. The sorting ensures that data gaps between the tags are minimized and that the tags are stored access-optimized for the processor.

Non-optimized blocks only exist for reasons of compatibility in S7-1200/1500.

Advantages

- The access is always as fast as possible, since the file storage is optimized by the system and is independent of the declaration.
- No danger of inconsistencies due to faulty, absolute accesses since the access is generally symbolic.
- Declaration changes do not lead to access errors since, for example, HMI accesses are performed symbolically.
- Individual tags can be specifically defined as “retain”.
- No settings in the instance data block are necessary. Everything is set in the assigned FB (e.g. retentivity).
- Memory reserves in the data block make it possible to change the actual values without any loss (see chapter 3.2.7 Downloading without reinitialization)

2.6.1 S7-1200: Setup of optimized blocks

Properties

- No data gaps are formed since larger tags are located at the beginning of the block and smaller ones at the end.
- Only the symbolic access exists for optimized blocks.
2.6.2 S7-1500: Setup of optimized blocks

1. Structures are stored separately and can thus be copied as one block.
2. Retentive data are stored in a separate area and can be copied as one block. In the event of a power failure, these data are stored CPU-internally. "MRES" resets these data to the start values stored in the load memory.

Properties

- No data gaps are formed since larger tags are located at the beginning of the block and smaller ones at the end.
- Fast access due the best possible storage in the processor (All tags are stored in a way so that the processor of the S7-1500 can directly read or write all tags with just one machine command).
- Boolean tags are stored as byte for faster access. The controller therefore does not have to mask the access.
2.6 Optimized blocks

- Optimized blocks have a memory reserve for reloading in running operation (see chapter 3.2.7 Downloading without reinitialization).
- Only the symbolic access exists for optimized blocks.

2.6.3 Best possible data storage in the processor on S7-1500

For reasons of compatibility to the first SIMATIC controllers the “Big-Endian” principle of data storage was adopted in the S7-300/400 controllers. The new S7-1500 controller generation always accesses 4 byte (32 bit) in “Little-Endian” sequence due to the changed processor architecture. This results in the following system-specific properties.

Figure 2-8: Data access of a S7-1500 controller

Table 2-3: Data access of a S7-1500 controller

<table>
<thead>
<tr>
<th>Standard block</th>
<th>Optimized block</th>
</tr>
</thead>
<tbody>
<tr>
<td>1. In the event of an unfavorable offset, the controller needs 2x16 bit accesses in order to be able to read a 4 byte value (e.g. REAL value). In addition the bytes have to be changed.</td>
<td>The controller stores the tags, access optimized. An access is performed with 32 bit (REAL). A changing of the bytes is not necessary.</td>
</tr>
<tr>
<td>2. The complete byte is read and masked per bit access. The complete byte is blocked for any other access.</td>
<td>Each bit is assigned a byte. When accessing, the controller does not have to mask the byte.</td>
</tr>
<tr>
<td>3. Maximum block size is 64kB.</td>
<td>Maximum block size can be up to 16MB.</td>
</tr>
</tbody>
</table>
Recommendation

- Always only use optimized blocks.
  - They do not require absolute addressing and can always be addressed with symbolic data (object related). Indirect addressing is also possible with symbolic data (see chapter 3.6.2 ARRAY data type and indirect field accesses).
  - The processing of optimized blocks in the controller is much faster than with standard blocks.
- Avoid the copying/assigning of data between optimized and non-optimized blocks. The required conversion between source and destination format requires high processing time.

Example: Setting optimized block access

The optimized block accesses for all newly created blocks for S7-1200/1500 is enabled by default. Block access can be set for OBs, FBs and global DBs. For instance DBs, the setting depends on the respective FB.

The block access is not reset automatically when a block is migrated from a S7-300/400 controller to a S7-1200/1500. You can change the block access later on to “optimized block access”. You need to recompile the program after changing the block access. If you change the FBs to “optimized block access”, the assigned instance data blocks are automatically updated.

Follow the instructions below, in order to set the optimized block access.

Table 2-4: Setting optimized block access

<table>
<thead>
<tr>
<th>Step</th>
<th>Instruction</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>Click the “Maximizes/minimizes the Overview” button in the project navigation.</td>
</tr>
<tr>
<td>2.</td>
<td>Navigate to “Program blocks”.</td>
</tr>
</tbody>
</table>
### 2.6 Optimized blocks

#### Instruction

<table>
<thead>
<tr>
<th>Step</th>
<th>This is where you see all blocks in the program and whether they are optimized or not. In this overview the “Optimized block access” status can be conveniently changed.</th>
</tr>
</thead>
</table>

#### Display of optimized and non-optimized blocks in the TIA Portal

In the two following figures the differences between an optimized and a non-optimized instance DB can be seen.

For a global DB there are the same differences.

**Figure 2-9: Optimized data block (without offset)**

<table>
<thead>
<tr>
<th>Name</th>
<th>Data type</th>
<th>Start value</th>
<th>Retain</th>
<th>Visible in</th>
<th>Setpoint</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input</td>
<td>Bool</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Input_bool_1</td>
<td>Int</td>
<td>1640</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Input_bool_2</td>
<td>Int</td>
<td>1640</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Input_word</td>
<td>Int</td>
<td>1640</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Input_byte_1</td>
<td>Int</td>
<td>1640</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Input_byte_2</td>
<td>Int</td>
<td>1640</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Output</td>
<td>Int</td>
<td>1640</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Output_bool_1</td>
<td>Int</td>
<td>1640</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Static</td>
<td>Int</td>
<td>1640</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Figure 2-10: Non-optimized data blocks (with offset)**

<table>
<thead>
<tr>
<th>Name</th>
<th>Data type</th>
<th>Offset</th>
<th>Start value</th>
<th>Retain</th>
<th>Visible in</th>
<th>Setpoint</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input</td>
<td>Bool</td>
<td>0.0</td>
<td>0.0</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Input_bool_1</td>
<td>Int</td>
<td>1.0</td>
<td>1640</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Input_bool_2</td>
<td>Int</td>
<td>2.0</td>
<td>1640</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Input_word</td>
<td>Int</td>
<td>4.0</td>
<td>1640</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Input_byte_1</td>
<td>Int</td>
<td>4.0</td>
<td>1640</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Input_byte_2</td>
<td>Int</td>
<td>4.0</td>
<td>1640</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Output</td>
<td>Int</td>
<td>8.0</td>
<td>1640</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Output_bool_1</td>
<td>Int</td>
<td>8.0</td>
<td>1640</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Static</td>
<td>Int</td>
<td>1640</td>
<td>1640</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

#### Table 2-5: Difference: optimized and non-optimized data block

<table>
<thead>
<tr>
<th>Optimized data block</th>
<th>Non-optimized data block</th>
</tr>
</thead>
<tbody>
<tr>
<td>Optimized data blocks are addressed symbolic. <strong>No “offset” is displayed.</strong></td>
<td>At non-optimized blocks an “offset” is displayed and can be used for addressing.</td>
</tr>
<tr>
<td>In optimized blocks <strong>every</strong> tag can be declared with “Retain”.</td>
<td>In non-optimized blocks only <strong>all or no</strong> tags can be declared with “Retain”.</td>
</tr>
</tbody>
</table>
The retentivity of tags of a global DB is defined directly in the global DB. The default setting is non-retentive.

The retentivity of tags of one instance is defined in the function block (not in the instance DB). These settings then apply to all instances of this FB.

### Access types for optimized and non-optimized blocks

The following table displays all access types to blocks.

**Table 2-6: Access types**

<table>
<thead>
<tr>
<th>Access type</th>
<th>Optimized block</th>
<th>Non-optimized block</th>
</tr>
</thead>
<tbody>
<tr>
<td>Symbolic</td>
<td>✓</td>
<td>✓</td>
</tr>
<tr>
<td>Indexed (fields)</td>
<td>✓</td>
<td>✓</td>
</tr>
<tr>
<td>Slice accesses</td>
<td>✓</td>
<td>✓</td>
</tr>
<tr>
<td>AT instruction</td>
<td>❌ (Alternatively: slice access)</td>
<td>✓</td>
</tr>
<tr>
<td>Direct absolute</td>
<td>❌ (Alternatively: ARRAY with index)</td>
<td>✓</td>
</tr>
<tr>
<td>Indirect absolute (pointer)</td>
<td>❌ (Alternatively: VARIANT / ARRAY with index)</td>
<td>✓</td>
</tr>
<tr>
<td>Downloading without reinitialization</td>
<td>✓</td>
<td>❌</td>
</tr>
</tbody>
</table>

**Note**

You will find further information in the following entries:

What differences should you watch out for between optimized data storage and the standard type of block access in STEP 7 (TIA Portal) V12?


What properties do you have to pay attention to in STEP 7 V11 for the instructions "READ_DBL" and "WRIT_DBL", when you are using DBs with optimized access?


### 2.6.4 Conversion between optimized and non-optimized tags

The general recommendation is to work with optimized tags. However, if you want to keep your previous programming in individual cases, this leads to a mixture of optimized and non-optimized data storage in the program.

The system recognizes the internal storage of each tag, no matter if structured (derived from a user-defined data type) or elementary (INT, LREAL, etc.).

In the case of type-identical allocations between two tags with different storage locations, the system converts automatically. In the case of structured tags, this conversion requires performance and should therefore be avoided, if possible.
2.6.5 Communication with optimized data

The interface (CPU, CM) transfers the data as they are arranged (no matter if optimized or not).

Figure 2-11: CPU-CPU communication

Send data can be:
- optimized
- not optimized
- Tag (any type)
- Buffer (byte array)

Receive data can be:
- optimized
- not optimized
- Tag (any type)
- Buffer (byte array)

Example

- A tag with data type PLC (data record) is to be transferred to a CPU.
- In the send CPU, the tag is interconnected as actual parameter with the communication block (TSEND_C).
- In the receive CPU, the receive data are assigned to a tag of the same type.
- In this case, it is possible to symbolically continue to work directly with the received data.

Note

Any tags or data blocks (derived from PLC data types) can be used as data records.

Note

It is also possible that the send and receive data are not defined identically:

<table>
<thead>
<tr>
<th>Send data</th>
<th>Receive data</th>
</tr>
</thead>
<tbody>
<tr>
<td>optimized</td>
<td>not optimized</td>
</tr>
<tr>
<td>not optimized</td>
<td>optimized</td>
</tr>
</tbody>
</table>
2.7 Block sizes

For S7-1200/1500 controllers the maximum size of blocks was significantly increased in the main memory.

Table 2-7: Block sizes

<table>
<thead>
<tr>
<th>Block type</th>
<th>Max. size</th>
<th>Max. number</th>
</tr>
</thead>
<tbody>
<tr>
<td>DB</td>
<td>S7-300/400</td>
<td>64 kB</td>
</tr>
<tr>
<td></td>
<td>S7-1200</td>
<td>64 kB</td>
</tr>
<tr>
<td></td>
<td>S7-1500</td>
<td>64 kB (non-optimized) 16 MB (optimized)</td>
</tr>
<tr>
<td></td>
<td>FC/FB</td>
<td>64 kB</td>
</tr>
<tr>
<td></td>
<td>FC/FB</td>
<td>64 kB</td>
</tr>
<tr>
<td></td>
<td>FC/FB</td>
<td>512 kB</td>
</tr>
<tr>
<td></td>
<td>FC/FB</td>
<td>7.999</td>
</tr>
<tr>
<td></td>
<td>FC/FB</td>
<td>65.535</td>
</tr>
<tr>
<td></td>
<td>FC/FB</td>
<td>65.535</td>
</tr>
<tr>
<td></td>
<td>DB</td>
<td>4.096</td>
</tr>
<tr>
<td></td>
<td>DB</td>
<td>6.000</td>
</tr>
<tr>
<td></td>
<td>DB</td>
<td>1.024</td>
</tr>
<tr>
<td></td>
<td>DB</td>
<td>6.000</td>
</tr>
</tbody>
</table>

Recommendation

- Use the DBs for S7-1500 controllers as data container of very large data volumes.
- Data volumes of > 64 kB can be stored in an optimized DB (max. size 16 MB) with S7-1500 controllers.

2.8 New data types for S7-1200/1500

S7-1200/1500 controllers support new data types in order to make programming more convenient. With the new 64 bit data types considerably larger and more accurate values can be used.

Note

You will find further information in the following entry:

How is the conversion of data types performed in the TIA Portal for the S7-1200/1500?


2.8.1 Elementary data types

Table 2-8: Integer data types

<table>
<thead>
<tr>
<th>Type</th>
<th>Size</th>
<th>Value range</th>
</tr>
</thead>
<tbody>
<tr>
<td>USint</td>
<td>8 bit</td>
<td>0 .. 255</td>
</tr>
<tr>
<td>SInt</td>
<td>8 bit</td>
<td>-128 .. 127</td>
</tr>
<tr>
<td>UInt</td>
<td>16 bit</td>
<td>0 .. 65535</td>
</tr>
<tr>
<td>UDInt</td>
<td>32 bit</td>
<td>0 .. 4.3 million</td>
</tr>
<tr>
<td>UInt*</td>
<td>64 bit</td>
<td>0 .. 18.4 billion</td>
</tr>
<tr>
<td>LInt*</td>
<td>64 bit</td>
<td>-9.2 billion .. 9.2 billion</td>
</tr>
</tbody>
</table>

* only for S7-1500
2.8 New data types for S7-1200/1500

Table 2-9: Floating-point decimal data types

<table>
<thead>
<tr>
<th>Type</th>
<th>Size</th>
<th>Value range</th>
</tr>
</thead>
<tbody>
<tr>
<td>Real</td>
<td>32 bit (1 bit signs, 8 bit exponent, 23 bit mantissa), accurate to 7 decimal places</td>
<td>-3.40e+38 .. 3.40e+38</td>
</tr>
<tr>
<td>LReal</td>
<td>64 bit (1 bit signs, 11 bit exponent, 52 bit mantissa), accurate to 15 decimal places</td>
<td>-1.79e+308 .. 1.79e+308</td>
</tr>
</tbody>
</table>

2.8.2 Date_Time_Long data type

Table 2-10: Structure of DTL (Date_Time_Long)

<table>
<thead>
<tr>
<th>Year</th>
<th>Month</th>
<th>Day</th>
<th>Weekday</th>
<th>Hour</th>
<th>Minute</th>
<th>Second</th>
<th>Nanosecond</th>
</tr>
</thead>
</table>

DTL always reads the current system time. Access to the individual values is through the symbolic names (e.g. My_Timestamp.Hour)

Advantages
- All partial areas (e.g. Year, Month, …) can be addressed symbolically.

Recommendation
Use the new DTL data type instead of LDT and address symbolically (e.g. My_Timestamp.Hour).

Note
You will find further information in the following entries:
In STEP 7 (TIA Portal), how can you input, read out and edit the date and time for the CPU modules of S7-300/S7-400/S7-1200/S7-1500?

Which functions are available in STEP 7 V5.5 and in TIA Portal for processing the data types DT and DTL?

2.8.3 VARIANT data type

A parameter of the VARIANT type is a pointer that can point to tags of different data types. In contrast to the ANY pointer the VARIANT is a pointer with type test. The target structure and source structure are checked at runtime and have to be identical.

VARIANT is used, for example, as input for communication blocks (TSEND_C).
Advantages

- Integrated type test
- Symbolic addressing for optimized blocks

Recommendation

- Use the VARIANT data type instead of the ANY pointer. Due to the integrated type test, errors are detected early on and due to the symbolic addressing the program code can be easily interpreted.
- As another alternative you can use the indexed ARRAYs for the ANY pointer (see chapter 3.6.2 ARRAY data type and indirect field accesses).

Note

You will find further information in the following entry:

How can memory areas be copied in STEP 7 (TIA Portal)?

How do you program the "VARIANT" data type for indirect addressing for the S7-1200 in STEP 7 (TIA Portal) V11?
2.9 Instructions

2.9.1 CALCULATE

With the CALCULATE instruction you can carry out mathematical calculations (e.g. \((IN1 + IN2) \times IN3\)) that are independent from the data type. The mathematical formula is programmed in the formula editor of the instruction.

Figure 2-13: CALCULATE instruction with formula editor

Note

For more information refer to the Online Help of the TIA Portal with the “CALCULATE” instruction.

Advantages

- A mathematical formula only needs one instruction
- Time saving due to simple configuration

Properties

- Supports bit sequences, integers, floating-point numbers
- Supports numerous mathematical functions (all basic arithmetic operations, trigonometric functions, rounding, logarithm, etc.)
- Number of inputs is extendable

Recommendation

- Always use the CALCULATE instruction for mathematical calculations instead of many calls of instructions, such as, e.g. ADD, SUB, etc.

2.9.2 MOVE instructions

STEP 7 (TIA) provides the following MOVE instructions. The instruction MOVE_BLK_VARIANT for S7-1200/1500 is new.
Table 2-11: Move instructions

<table>
<thead>
<tr>
<th>Instruction</th>
<th>Usage</th>
<th>Properties</th>
</tr>
</thead>
</table>
| MOVE                 | Copy value          | • Copies the content of the parameter at the IN input to the parameter of the OUT output.  
                          |                     | • The parameters at the input and output must be of the same data type.  
                          |                     | • The parameters can also be structured tags (PLC data types). |
| MOVE_BLK             | Copy array          | • Copies the content of an array to another array.  
                          |                     | • The source and target array must be of the same data type. |
| UMOVE_BLK            | Copy array without interruption | • Copies the content of an array without any interruption to another array.  
                          |                     | • The source and target array must be of the same data type. |
| MOVE_BLK_VARIANT     | Copy array          | • Copies one or several structured tag(s) (PLC data types).  
                          |                     | • Recognizes data types at runtime.  
                          |                     | • Supplies detailed error information.  
                          |                     | • Apart from the elementary and structured data types, also PLC data types, arrays, and array DBs are supported |

Note

UMOVE_BLK: The copy process cannot be interrupted by another activity of the operating system. Therefore, the alarm reaction times of the CPU might increase during processing of the instruction "Copy array without interruption".

For the complete description of the MOVE instructions, please refer to the TIA Portal Online Help.

2.9.3 RUNTIME

Using the "RUNTIME" instruction you measure the runtime of the complete program, single blocks or the command sequences. You can call this instruction in SCL (S7-1200/S7-1500) and in STL (S7-1500).

NOTE

You will find further information in the following entry:

With S7-1200/S7-1500, how do you measure the time of a program section or the complete program cycle at runtime?  
2.10 Symbolic and comments

Advantages

You can make the code easy to understand and readable for your colleagues with the use of symbolic names and comments in your program. The complete symbolic is saved together with the program code during the download to the controller and allows fast maintenance of the plant when no offline project is at hand.

Recommendation

- Use the comments in the programs in order to improve readability. Network title comments are visible even if networks are collapsed.
- Design the program code in a way so that colleagues can understand the program straight away.

In the following example you can see the extensive options for commenting the program editors.

Example

In the following figure you can see the options for commenting in the LAD editor (same functionality in FDB).

Figure 2-14: Commenting in the user program (LAD)

The following comments are possible:

3. Block comment
4. Network title comment
5. Network comment
6. Comment on instructions, blocks and functions (open, close, etc.)

In the programming languages SCL and STL, it can be commented with // in every row.
Example

Filling level:= Radius * Radius * PI * height;
// calculation of the filling level for medium tank

2.11 System constants

For S7-300/400 controllers the identification of hardware and software components is performed by logic address or diagnostic addresses.

For S7-1200/1500 the identification is by system constants. All hardware and software components (e.g. interfaces, modules, OBs, ...) of the S7-1200/1500 controllers have their own system constants. The system constants are automatically created during the setup of the device configuration for the central and distributed I/O.

Advantages
- You can address via module names instead of hardware identification.

Recommendation
- Assign function-related module names in order to identify the modules easily during programming.

Example

In the following example you can see how system constants are used in the user program.

Figure 2-15: “System constants” in the user program
1. System constants of a controller can be found in the “PLC tags – Default tag table” folder.
2. The system constants are in a separate tab in the “Default tag table”.
3. In this example the symbolic name “Robot_arm_left” was assigned for a DI module. You can also find the module under this name in the system constant tab. In the user program “Robot_arm_left” is interconnected with the “GET_DIAG” diagnostic block.

**Note**
You will find further information in the following entry:
What meaning do the system constants have for the S7-1200/1500 in STEP 7 (TIA Portal)?

**2.12 Internal reference ID for controller and HMI tags**

STEP 7, WinCC, Startdrive, Safety and others integrate into the joint data base of the TIA Portal engineering framework. Changes of data are automatically accepted in all the locations in the user program, independent from whether this happens in a controller, a panel or a drive. Therefore no data inconsistencies can occur.

If you create a tag, the TIA Portal automatically creates a unique reference ID. The reference ID cannot be viewed or programmed by you. This procedure is internal referencing. When changing tags (address), the reference ID remains unchanged.

In the figure below the internal reference to the data is displayed schematically.

Figure 2-16: Internal reference ID for PLC and HMI

<table>
<thead>
<tr>
<th>PLC Symbol name</th>
<th>Absolute address</th>
<th>Internal PLC reference ID</th>
<th>Internal HMI reference ID</th>
<th>HMI Symbol name</th>
<th>Access mode</th>
<th>Connection with PLC</th>
</tr>
</thead>
<tbody>
<tr>
<td>Motor_1</td>
<td>I0.0</td>
<td>000123</td>
<td>009876</td>
<td>Motor_1</td>
<td>&lt;symbolic access&gt;</td>
<td>PLC_1</td>
</tr>
<tr>
<td>Valve_2</td>
<td>Q0.3</td>
<td>000138</td>
<td>000578</td>
<td>Valve_2</td>
<td>&lt;symbolic access&gt;</td>
<td>PLC_1</td>
</tr>
</tbody>
</table>

**NOTE**
The ID will be invalid if …
- name is changed.
- type is changed.
- tag is deleted.
2 S7-1200/1500 Innovations

2.13 STOP mode in the event of errors

Advantages

- You can rewire tags without changing internal relations. The communication between controller, HMI and drive also remains unchanged.
- The length of the symbolic name does not have an influence on the communication load between controller and HMI.

Properties

If you change addresses of PLC tags, you only have to reload the controller. It is not necessary to reload the HMI devices, since internally, the system addresses with the reference IDs (see Figure 2-17: Changing address or adding row).

Figure 2-17: Changing address or adding row

PLC Tags

Adding row & PLC

DB Elements

Changing address & PLC

2.13 STOP mode in the event of errors

In comparison to S7-300/400 there are fewer criteria with the S7-1200/1500 that lead to the “STOP” mode.

Due to the changed consistency check in the TIA Portal, the “STOP” mode for S7-1200/1500 controllers can already be excluded in advance in most cases. The consistency of program blocks is already checked when compiling in the TIA Portal. This approach makes the S7-1200/1500 controllers more fault tolerant to errors than their predecessors.

Advantages

There are only three fault situations that put the S7-1200/1500 controllers into the STOP mode. This makes the programming of the error management clearer and easier.

Properties

Table 2-12: Responses to errors of S7-1200/1500

<table>
<thead>
<tr>
<th>Error</th>
<th>S7-1200</th>
<th>S7-1500</th>
</tr>
</thead>
<tbody>
<tr>
<td>1. Cycle monitoring time exceeded once</td>
<td>RUN</td>
<td>STOP, when OB80 is not configured</td>
</tr>
<tr>
<td>2. Cycle monitoring time exceeded twice</td>
<td>STOP</td>
<td>STOP</td>
</tr>
<tr>
<td>3. Programming errors</td>
<td>RUN</td>
<td>STOP, when OB121 is not configured</td>
</tr>
</tbody>
</table>
Error OBs:

- OB80 “Time error interrupt” is called by the operating system when the maximum cycle time of the controller is exceeded.
- OB121 “Programming error” is called by the operating system when an error occurs during program execution.

For every error, in addition, an entry is automatically created in the diagnostic buffer.

**Note**

For S7-1200/1500 controllers there are other programmable error OBs (diagnostic error, module rack failure, etc.).

More information on error responses of S7-1200/1500 can be found in the online help of the TIA Portal under “Events and OBs.”
3 General Programming

3.1 Operating system and user program

SIMATIC controllers consist of operating system and user program.

- The operating system organizes all functions and sequences of the controller that are not connected with a specific control task (e.g. handling of restart, updating of process image, calling the user program, error handling, memory management, etc.). The operating system is an integral part of the controller.

- The user program includes all blocks that are required for the processing of your specific automation task. The user program is programmed with program blocks and loaded onto the controller.

Figure 3-1: Operating system and user program

For SIMATIC controllers the user program is always executed cyclically. The "Main" cycle OB already exists in the "Program blocks" folder after a controller was created in STEP 7. The block is processed by the controller and recalled in an infinite loop.

3.2 Program blocks

In STEP 7 (TIA Portal) there are all familiar block types from the previous STEP 7 versions:

- Organization blocks
- Function blocks
- Functions
- Data blocks

Experienced STEP 7 users will know their way around straight away and new users can very easily get familiar with the programming.

Advantages

- You can give your program a good and clear structure with the different block types.
- Due to a good and structured program you get many function units that can be multiply reused within a project and also in other projects. These function units then usually only differ by a different configuration (see chapter 3.2.8 Reusability of blocks).
3 General Programming

3.2 Program blocks

- You project or your plant becomes more transparent. Error states in a plant can be more easily detected, analyzed and removed. The maintainability of your plant becomes easier. This is also the case for errors in programming.

Recommendation

- Structure your automation task.
- Divide the entire function of your plant into individual areas and form sub-function units. Divide these sub function units again into smaller units and functions. Divide until you get functions that you can use several times with different parameters.
- Specify the interfaces between the function units. Define the unique interfaces for functionalities that are to be delivered by “third party companies”.

All organization blocks, function blocks and functions can be programmed with the following languages:

<table>
<thead>
<tr>
<th>Programming language</th>
<th>S7-1200</th>
<th>S7-1500</th>
</tr>
</thead>
<tbody>
<tr>
<td>Ladder (LAD)</td>
<td>✓</td>
<td>✓</td>
</tr>
<tr>
<td>Function block diagram (FBD)</td>
<td>✓</td>
<td></td>
</tr>
<tr>
<td>Structured control language (SCL)</td>
<td>✓</td>
<td>✓</td>
</tr>
<tr>
<td>Graph</td>
<td>planned</td>
<td>✓</td>
</tr>
<tr>
<td>Statement list (STL)</td>
<td></td>
<td>✓</td>
</tr>
</tbody>
</table>

3.2.1 Organization blocks (OB)

OBs are the interface between the operating system and the user program. They are called by the operating system and control, e.g. the following processes:

- Startup behavior of the controller
- Cyclic program processing
- Interrupt-controlled program processing
- Error handling
Depending on the controller a number of different OB types are available.

**Properties**

- OBs are called by the operating system of the controller
- Several Main OBs can be created in a program. The OBs are processed sequentially by OB number.

**Figure 3-3: Using several Main OBs**

**Recommendation**

- Encapsulate the different program parts which should maybe be replaceable from controller to controller, into several Main OBs.
- Avoid the communication between the different Main OBs. They can then be used independent from each other. If you nevertheless exchange data between the individual main OBs, use the global DBs (see chapter 4.2 No bit memory but global data blocks).
- Divide all program parts that belong to each other into folders and store them for reusability in the project or global library.
Figure 3-4: Storing program parts in order in the project library

For further information, please refer to chapter 3.7 Libraries.

Note
You will find further information in the following entry:
Which organization blocks can be used in STEP 7 (TIA Portal)?

3.2.2 Functions (FC)

Figure 3-5: "Add new block" dialog (FC)

FCs are blocks without cyclic data storages. This is why the values of block parameters cannot be saved until the next call and has to be provided with actual parameters when called.

Properties
- FCs are blocks without cyclic data storages.
3 General Programming

3.2 Program blocks

- Temporary and out tags are undefined when called in non-optimized blocks. In optimized blocks, the values are always preset to the default value (S7-1500 and S7-1200 Firmware V4). Thus, the resulting behavior is not accidental but reproducible.

- In order to permanently save the data of an FC, the functions of the global data blocks are available.

- FCs can have several outputs.

- The function value can be directly reused in SCL in a formula.

**Recommendation**

- Use the functions for continuously recurring applications that are called several times in different locations of the user program.

- Use the option to directly reuse the function value in SCL.

  \[ \text{<Operand>} := \text{<FC name>} (\text{parameter list}); \]

**Example**

In the following example a mathematical formula is programmed in a FC. The result of the calculation is directly declared as return value and the function value can be directly reused.

**Table 3-2: Reusing function value**

<table>
<thead>
<tr>
<th>Step</th>
<th>Instruction</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>Create an FC with the mathematical formula (circular segment) and define the “Return” value as the result for the formula.</td>
</tr>
</tbody>
</table>

![Diagram of FC with mathematical formula]

<table>
<thead>
<tr>
<th>Step</th>
<th>Instruction</th>
</tr>
</thead>
<tbody>
<tr>
<td>2.</td>
<td>Call the FC with the circular segment calculation in any block (SCL).</td>
</tr>
</tbody>
</table>

![Diagram of calling the FC in SCL]

\[ \text{<Operand>} := \text{<FC name>} (\text{parameter list}); \]
3.2 Program blocks

3.2.3 Function blocks (FB)

FBs are blocks with cyclic data storage, in which values are permanently stored. The cyclic data storage is realized in an instance DB.

Properties

- FBs are blocks with cyclic data storage.
- Temporary and out tags are undefined when called in non-optimized blocks. In optimized blocks, the values are always preset to the default value (S7-1500 and S7-1200 Firmware V4). Thus, the resulting behavior is not accidental but reproducible.
- Static tags keep the value from cycle to cycle

Recommendation

- Use the function blocks in order to create subprograms and structure the user program. A function block can also be called several times in different locations of the user program. This makes programming of frequently recurring program parts easier.
- If function blocks are applied multiply in the user program, use separate instances, preferably multi-instances.
3 General Programming

3.2 Program blocks

3.2.4 Instances

The call of a function block is called instance. The data with which the instance is working is saved in an instance DB.

Instance DBs are always created according to the specifications in the FB interface and can therefore not be changed in the instance DB.

Figure 3-8: Structure of the interfaces of an FB

The instance DB consists of a permanent memory with the interfaces input, output, InOut and static. In a volatile memory (L stack) temporary tags are stored. The L stack is always only valid for one cycle. I.e. temporary tags have to be initialized in each cycle.

Properties

- Instance DBs are always assigned to a FB.
- Instance DBs do not have to be created manually in the TIA Portal and are created automatically when calling an FB.
- The structure of the instance DB is specified in the appropriate FB and can only be changed there.

Recommendation

- Program it in a way so that the data of the instance DB can only be changed by the appropriate FB. This is how you can guarantee that the block can be used universally in all kinds of projects.

For further information, please refer to chapter 3.4 Block interfaces as data exchange.

3.2.5 Multi-instances

With multi-instances called function blocks can store their data in the instance data block of the called function block. I.e. if another function block is called in a function block, it saves its data in the instance DB of the higher-level FBs. The functionality of the called block is thus maintained even if it is transferred.

The following figure shows an FB that uses another FB (“IEC Timer”). All data is saved in a multi instance DB. It is thus possible to create a block with an independent time behavior, for example, a clock generator.
3 General Programming
3.2 Program blocks

Advantages
- Reusability
- Multiple calls are possible
- Clearer program with fewer instance DBs
- Simple copying of programs
- Good options for structuring during programming

Properties
- Multi-instances are memory areas within instance DBs.

Recommendation
Use multi-instances in order to …
- reduce the number of instance DBs.
- create reusable and clear user programs.
- program local functions e.g. timer, counter, edge detection.

Example
If you require the time and counter function, use the “IEC Timer” blocks and the “IEC Counter” blocks instead of the absolutely addressed SIMATIC Timer. If possible, also always use multi-instances here. This keeps the number of blocks in the user program low.
3 General Programming

3.2 Program blocks

Figure 3-10: Library of the IEC Timer

Note

You will find further information in the following entry:

How do you declare the timers and counters for the S7-1500 in STEP 7 (TIA Portal) V12?


3.2.6 Global data blocks (DB)

Figure 3-11: "Add new block" dialog (DB)

Variable data is located in data blocks that are available to the entire user program.
3.2 Program blocks

Advantages
- Well structured memory area
- High access speed

Properties
- All blocks in the user program can access global DBs.
- The structure of the global DBs can be arbitrarily made up of all data types.
- Global DBs are either created via the program editor or according to a previously created "user-defined PLC data type" (see chapter 3.6.3 STRUCT data type and PLC data types).

Recommendation
- Use the global DBs when data is used in different program parts or blocks.

Note
You will find further information in the following entry:
What access types, value columns and operating options are there for the global data blocks in STEP 7 V12?

3.2.7 Downloading without reinitialization

In order to change user programs that already run in a controller, S7-1200 (firmware V4.0) and S7-1500 controllers offer the option to expand the interfaces of optimized function or data blocks during operation. You can load the changed blocks without setting the controller to STOP and without influencing the actual values of already loaded tags.
Execute the following steps whilst the controller is in RUN mode.

1. Enable “Downloading without reinitialization”
2. Insert new defined tags in existing block
3. Load block into controller

**Advantages**

- Reloading of new defined tags without interrupting the running process. The controller stays in “RUN” mode.

**Properties**

- Downloading without reinitialization is only possible for optimized blocks.
- New defined tags will be initialized. The remaining tags keep their current values.
- A block with reserve requires more memory space in the controller.
- The memory reserve depends on the work memory of the controller; however it is max. 2 MB.
- It is assumed that a memory reserve has been defined for block.
- By default the memory reserve is set to 100 byte.
- The memory reserve is defined individually for every block.
- The blocks can be variably expanded.

**Recommendation**

- Define a memory reserve for blocks that are to be expanded during commissioning (e.g. test blocks). The commissioning process is not interrupted by download of new defined tags. The current values of already existing variables are kept.
Example: Setting memory reserve in block

The following table describes how you can set the memory reserve for the downloading without reinitializing.

Table 3-3: Setting memory reserve

<table>
<thead>
<tr>
<th>Step</th>
<th>Instruction</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>Right-click any optimized block in the project navigator and select “Properties”.</td>
</tr>
<tr>
<td>2.</td>
<td>Click “Download without reinitialization”.</td>
</tr>
<tr>
<td>3.</td>
<td>Enter the desired memory reserve for “Memory reserve”.</td>
</tr>
<tr>
<td>5.</td>
<td>Confirm with “OK”.</td>
</tr>
</tbody>
</table>

Note

You can also set a default value for the size of the memory reserve for new blocks in the TIA portal.

In the menu bar, navigate to "Options – Settings" and then to "PLC programming – General – Download without reinitialization".
3 General Programming

3.2 Program blocks

Example: Downloading without reinitialization

In the following example it is displayed how to download without reinitialization.

Table 3-4 Downloading without reinitialization

<table>
<thead>
<tr>
<th>Step</th>
<th>Instruction</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>Requirement: a memory reserve has to be set (see above)</td>
</tr>
<tr>
<td>2.</td>
<td>Open, e.g. an optimized global DB.</td>
</tr>
<tr>
<td>3.</td>
<td>Click the “Download without reinitialization” button and confirm the dialog with “OK”</td>
</tr>
<tr>
<td>4.</td>
<td>Add a new tag (retentive tags are also possible).</td>
</tr>
<tr>
<td>5.</td>
<td>Download the block to the controller.</td>
</tr>
</tbody>
</table>
| 6.   | Result:  
- Actual values of the block remain |

Note

Further information can be found in the online help of the TIA Portal under “Loading block extensions without reinitialization”.

You will find further information in the following entry:

What options does the S7-1500 provide for downloading data in RUN?  

3.2.8 Reusability of blocks

The block concept offers you a number of options to program in a structured and effective way.

Advantages

- Blocks can be used universally in any location of the user program.
- Blocks can be used universally in different projects.
3 General Programming

3.3 Block interface types

- When every block receives an independent task, a clear and well-structured user program is automatically created.
- There are clearly fewer sources of errors
- Simple error diagnostic possible.

Recommendation

If you want to reuse the block, please note the following recommendations:

- Always look at blocks as encapsulated functions. I.e. each block represents a completed partial task within of the entire user program.
- Use several cyclic Main OBs to group the plant parts.
- Always execute a data exchange between the blocks via its interfaces and not via its instances (see chapter 3.4.1 Block interfaces as data exchange).
- Do not use project-specific data and avoid the following block contents:
  - Access to global DBs and use of individual instance DBs
  - Access to tags
  - Access to global constants
- Reusable blocks have the same requirements as know-how-protected blocks in libraries. This is why you have to check the blocks for reusability based on the “Block can be used as know-how protected library element” block property. Compile the block before the check.

Figure 3-14: Block attributes

3.3 Block interface types

FBs and FCs have three different interface types: In, InOut and Out. Via these interface types the blocks are provided with parameters. The parameters are processed and output again in the block. There are two different options for this parameter transfer.

3.3.1 Call-by-value with In interface type

When calling the block, the value of the actual parameter is copied onto the input parameter of the block for the In interface type. For this, additional memory is required.
3 General Programming

3.4 Storage concept

Properties
- Each block displays the same behavior with connected parameters
- Values are copied when calling the block

3.3.2 Call-by-reference with InOut interface type

When calling the block the address of the actual parameter of the Input parameter is referenced for the InOut interface type. For this, no additional memory is required.

Properties
- Each block displays the same behavior with connected parameters
- Actual parameters are referenced with the block call

Recommendation
- Generally use the InOut interface type for structured tags (e.g. of the ARRAY, STRUCT, STRING, type…) in order to avoid enlarging the required data memory unnecessarily.

3.4 Storage concept

For STEP 7 there is generally the difference between the global and local memory area. The global memory area is available for each block in the user program. The local memory area is only available within the respective block.

3.4.1 Block interfaces as data exchange

If you are encapsulating the functions and program the data exchange between the blocks only via the interfaces, you will clearly have advantages.
3.4 Storage concept

Advantages

- Program can be made up modularly from ready blocks with partial tasks.
- Program is easy to expand and maintain.
- Program code is easier to read since there are no hidden cross accesses.

Recommendation

- If possible, only use the local tags. This is how the blocks can be used universally and modularly.
- Use the data exchange via the block interfaces (In, Out, InOut), to ensure the reusability of blocks.
- Only use the instance data blocks as local memory for the respective function block. Other blocks shall not be written into instance data blocks.

Figure 3-17: Avoiding accesses to instance data blocks

If only the block interfaces are used for the data exchange it can be ensured that all blocks can be used independent from each other.

Figure 3-18: Block interfaces for data exchange

3.4.2 Global memory

Memories are called global when they can be accessed from any location of the user program. There are hardware-dependent memories (e.g. bit memory, timers, counters, etc.) and global DBs. For hardware-dependent memory areas there is the danger that the program may not be portable to any controller because the areas there may already be used. This is why you should use global DBs instead of hardware-dependent memory areas.
3 General Programming

3.4 Storage concept

Advantages

- User programs can be used universally and independent from the hardware.
- The user program can be structured modularly without dividing bit memory address areas for different users.
- Optimized global DBs are clearly more powerful than the bit memory address area that is not optimized for reasons of compatibility.

Recommendation

- Do not use any bit memory and use global DBs instead.
- Avoid hardware-dependent memory, such as, for example, clock memory or counter. Use the IEC counter and timer in connection with multi-instances instead (see chapter 3.2.5 Multi-instances). The IEC timers can be found under “Instructions – Basic Instructions – Timer operations”.

Figure 3-19: IEC Timers

3.4.3 Local memory

- Static tags
- Temporary tags

Recommendation

- Use the static tags for values that are required for the next cycle.
- Use the temporary tags as cache memory in current cycle. The access time for temporary tags is shorter than for static ones.

Note

Optimized blocks: Temporary tags are initialized in any block call with the "default value" (S7-1500 und S7-1200 Firmware V4).
Non-optimized blocks: Temporary tags are undefined for each call of the block.
3.4 Storage concept

3.4.4 Access speed of memory areas

STEP 7 offers different options of memory accesses. For system-related reasons there are faster and slower accesses to different memory areas.

Figure 3-20: Different memory accesses

<table>
<thead>
<tr>
<th>Access speed</th>
<th>fast</th>
<th>intermediate</th>
<th>slow</th>
</tr>
</thead>
<tbody>
<tr>
<td>Temporary tags</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Non-retain tags</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Non-structured elementary data type FC parameter</td>
<td>1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Indexed accesses with runtime tindex</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Accesses to checks for at runtime require (register, indirect and indirect DB accesses)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Access to non-optimized blocks</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Fastest accesses in the S7-1200/1500 in descending order

1. Optimized blocks: Temporary tags, parameters of an FC and FB, non-retentive static tags
2. Optimized blocks whose accesses for compiling are known:
   - Retentive FB tags
   - Optimized global DBs
3. Access to non-optimized blocks
4. Indexed accesses with index that was calculated at runtime (e.g. Motor [i])
5. Accesses that require checks at runtime
   - Accesses to DBs that are created at runtime or which were opened indirectly (e.g. OPN DB[i])
   - Register access or indirect memory access
6. Copying of structures between optimized and non-optimized blocks (apart from Array of Bytes)
3.5 Retentivity

In the event of a failure of the power supply, the controller copies the retentive data with its buffer energy from the controller’s work memory to a non-volatile memory. After restarting the controller, the program processing is resumed with the retentive data. Depending on the controller, the data volume for retentivity has different sizes.

Table 3-5: Retentive memory for S7-1200/1500

<table>
<thead>
<tr>
<th>Controller</th>
<th>Usable retentive memory for bit memory, times, counters, DBs and technology objects</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU 1211C</td>
<td>10 Kbytes</td>
</tr>
<tr>
<td>CPU 1212C</td>
<td>10 Kbytes</td>
</tr>
<tr>
<td>CPU 1214C</td>
<td>10 Kbytes</td>
</tr>
<tr>
<td>CPU 1215C</td>
<td>10 Kbytes</td>
</tr>
<tr>
<td>CPU 1511-1 PN</td>
<td>88 Kbytes</td>
</tr>
<tr>
<td>CPU 1513-1 PN</td>
<td>88 Kbytes</td>
</tr>
<tr>
<td>CPU 1516-3 PN/DP</td>
<td>472 Kbytes</td>
</tr>
</tbody>
</table>

Table 3-6: Differences of S7-1200 and S7-1500

<table>
<thead>
<tr>
<th>S7-1200</th>
<th>S7-1500</th>
</tr>
</thead>
<tbody>
<tr>
<td>Retentivity can only be set for bit memory</td>
<td>Retentivity can be set for bit memory, times and counters</td>
</tr>
</tbody>
</table>

Advantages

- Retentive data maintain their value when the controller goes to STOP and back to RUN or in the event of a power failure and a restart of the controller.

Properties

For elementary tags of an optimized DB the retentivity can be set separately. Non-optimized data blocks can only be defined completely retentive or non-retainive.

The retentive data can be deleted with the actions “memory reset” or "Reset to factory settings" via:

- Operating switch on the controller (MRES)
- Display of the controller
- Online via STEP 7 (TIA Portal)

Recommendation

- Avoid the setting “Set in IDB”. Always set the retentive data in the function block and not in the instance data block.

The “Set in IDB” setting increases the processing time of the program sequence. Always either select “Non-retain” or “Retain” for the interfaces in the FB.
### 3.5 Retentivity

**Example: Retentive of PLC tags**

The setting of the retentive data is performed in the tables of the PLC tags, function blocks and data blocks.

**Figure 3-23: Setting of the retentive tags in the table of PLC tags**

Retentivity can be set from address 0 onward! e.g. from MB0, T0 or C0

**Example: Retentive counter**

You can also declare instances of functions (timer, counter, etc.) retentive. As already described in chapter 3.2.5 Multi-instances.
NOTE
If the retentive memory on the PLC is not sufficient, it is possible to store data in the form of data blocks that are only located in the load memory of the PLC. The following entry is described by taking the example of an S7-1200. This programming also works for S7-1500.

You will find further information in the following entry:

In STEP 7 V11, how do you configure data blocks with the "Only store in load memory" attribute for an S7-1200?

3.6 Symbolic addressing

3.6.1 Symbolic instead of absolute addressing

The TIA Portal is optimized for symbolic programming. This results in many advantages. Due to the symbolic addressing you can program without having to pay attention to the internal data storage. The controller handles where the best possible storage is for the data. You can therefore completely concentrate on the solution for your application task.

Advantages

- Easier to read programs through symbolic tag names
- Automatic update of tag names at all usage locations in the user program
- Memory storage of the program data does not have to be manually managed (absolute addressing)
- Powerful data access
- No manual optimization for performance or program size reasons required
- IntelliSense for fast symbol input
- Fewer program errors due to type checking (validity of data types is checked for all accesses)
3 General Programming

3.6 Symbolic addressing

Recommendation

- “Don’t bother about the organization of the data storage”
- “Think” symbolically. Enter the “descriptive” name for each function, tag or data, such as, for example, Pump_boiler_1, heater_room_4, etc. This is how a generated program can easily be read without requiring many comments.
- Give all the tags used a direct symbolic name and define it afterwards with a right-click.

Example

Table 3-7: Example for creating symbolic tags

<table>
<thead>
<tr>
<th>Step</th>
<th>Instruction</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>Open the program editor and open any block.</td>
</tr>
<tr>
<td>2.</td>
<td>Enter a symbolic name directly at the input of an instruction.</td>
</tr>
<tr>
<td>3.</td>
<td>Right-click next to the block and select “Define tag…” in the context menu.</td>
</tr>
</tbody>
</table>
3 General Programming

3.6 Symbolic addressing

<table>
<thead>
<tr>
<th>Step</th>
<th>Instruction</th>
</tr>
</thead>
<tbody>
<tr>
<td>4.</td>
<td>Define the tag.</td>
</tr>
</tbody>
</table>

There is an elegant method to save time, if you want to define several tags in a network. Assign all tag names first of all. Then define all tags at the same time with the dialog of step 4.

**Note**
You will find further information in the following entry:

Why is universal definition and utilization of symbols in STEP 7 (TIA Portal) V12 obligatory for the S7-1500?

3.6.2 ARRAY data type and indirect field accesses

The ARRAY data type represents a data structure that consists of several elements of the same data type. The ARRAY data type is suitable, for example, for the storage of recipes, material tracking in a queue, cyclic process acquisition, protocols, etc.

Figure 3-25: ARRAY with 10 elements of the Integer (INT) data type

You can indirectly access individual elements in the ARRAY with a runtime tag (array ["index"]).
Advantages

- Simple access since the data type of the ARRAY elements is irrelevant for the access.
- No complicated pointer creation required
- Fast creation and expansion possible
- Useable in all programming languages

Properties

- Structured data type
- Data structure made of fixed number of elements of the same data type
- ARRAYS can be created also multi-dimensional
- Possible indirect access with runtime tag with dynamic index calculation at runtime

Recommendation

- Use ARRAY for indexed accesses instead of pointer (e.g. ANY pointer). This makes it easier to read the program since an ARRAY is more meaningful with a symbolic name than a pointer in a memory area.
- As run tag use the INT data type as temporary tag for highest performance.
- Use the "MOVE_BLK" instruction to copy parts of an ARRAY into another one.
- Use the "GET_ERR_ID" instruction to catch access errors within the Array.

Note

You will find further information in the following entry:

How do you implement an array access with an S7-1500 with variable index?
3.6 Symbolic addressing

3.6.3 STRUCT data type and PLC data types

The STRUCT data type represents a data structure which is made up of elements of different data types. The declaration of a structure is performed in the respective block.

Figure 3-27: Structure with elements with different data types

In comparison to structures, PLC data types are defined across the controller in the TIA Portal and can be centrally changed. All usage locations are automatically updated.

PLC data types are declared in the “PLC data types” folder in the project navigation before being used.

Figure 3-28: PLC data types

Advantages

- A change in a PLC data type is automatically updated in all usage locations in the user program.

Recommendation

- Use the PLC data types to summarize several associated data, such as, e.g. frames or motor data (setpoint, speed, rotational direction, temperature, etc.)
- Always use PLC data types instead of structures for the multiple uses in the user program.
- Use the PLC data types for structuring into data blocks.
- Use the PLC data types in order to specify a structure for a data block. The PLC data type can be used for any number of DBs. You can easily and conveniently create as many DBs of the same structure and adjust them centrally on the PLC data type.
3.6 Symbolic addressing

Note
You will find further information in the following entries:

How do you initialize structures into optimized memory areas for the S7-1500 STEP 7 (TIA Portal)?

How do you create a PLC data type for an S7-1500 controller?

In STEP 7 (TIA Portal) V12, how do you apply your own data types (UDT)?

Why should whole structures instead of many single components be transferred for the S7-1500 when a block is called?

3.6.4 Access to I/O areas with PLC data types

With S7-1500 controllers, you can create PLC data types and use them for structured access to inputs and outputs.

Figure 3-29: Access to I/O areas with PLC data types

1. PLC data type with all required data
2. PLC tag of the type of the created PLC data type and start address of the I/O data area (%Ix.0 or %Qx.0, e.g., %I0.0, %Q12.0, …)
3. Transfer of the PLC tag as actual parameter to the function block
4. Input of the function block is of the type of the created PLC data type
3.6 Symbolic addressing

Advantages
- High programming efficiency
- Easy multiple usability thanks to PLC data types

Recommendation
- Use PLC data types for access to I/O areas, for example, to symbolically receive and send drive telegrams.

3.6.5 Slice access

For S7-1200/1500 controllers, you can access the memory area of tags of the Byte, Word, DWord or LWord data type. The division of a memory area (e.g. byte or word) into a smaller memory area (e.g. Bool) is also called slice. In the figure below displays the symbolic bit, byte and word accesses to the operands.

Figure 3-30: Slice access

Advantages
- High programming efficiency
- No additional definition in the tag declaration required
- Simple access (e.g. control bits)

Recommendation
- Use the slice access rather than AT construct via accessing certain data areas in operands.

Note
You will find further information in the following entry:
How in STEP 7 (TIA Portal) can you access the unstructured data types bit-by-bit, byte-by-byte or word-by-word and symbolically?
3.7 Libraries

With the TIA Portal you can create independent libraries from different project elements that can be easily reused.

Advantages

- Simple storage for the data configured in the TIA Portal:
  - Complete devices (controller, HMI, drive, etc.)
  - Controller programs, blocks, tags, monitoring tables
  - HMI image, HMI tags, scripts, etc.
- Cross-project exchange via libraries
- Central update function of library elements
- Versioning library elements
- Fewer error sources when using control blocks through system-supported consideration of dependencies

Recommendations

- Create the master copies for easy reusability of blocks, hardware configurations, HMI images, etc.
- Create the types for the system-supported reusability of library elements:
  - Versioning of blocks
  - Central update function of all usage locations
- Use the global library for the exchange with other users or as central storage for the simultaneous use of several users.

3.7.1 Types of libraries and library elements

Generally there are two different types of libraries:

- "Project library"
- "Global library".

The content consists of two storage types each:

- "Types"
- "Master Copies"
3 General Programming

3.7 Libraries

Figure 3-31: Libraries in the TIA Portal

- (1) "Project library"
  - Integrated in the project and managed with the project
  - Allows the reusability within the project

- (2) "Global library"
  - Independent library
  - Use within several projects possible

A library includes two different types of storage of library elements:

- (3) "Master copies"
  - Copy of configuration elements in the library (e.g. blocks, hardware, PLC tag tables, etc.)
  - Copies are not connected with the elements in the project.
  - Master copies can also be made up several configuration elements.

- (4) "Types"
  - Types are connected with your usage locations in the project. When types are changed, the usage locations in the project can be updated automatically.
  - Supported types are controller blocks (FCs, FBs), PLC data types, HMI images, HMI faceplates, HMI UDT, scripts).
  - Subordinate elements are automatically typified.
  - Types are versioned: Changes can be made by creating a newer version.
  - There can only be one version of a used type within a controller.
3.7 Libraries

3.7.2 Type concept

The type concept allows the creation of standardized automation functions that you can use in several plants or machines. The type concept supports you with versioning and updating functions.

You can use types from the library in the user program. This offers the following advantages:

Advantages

- Central update of all usage locations in the project
- Unwanted modifications of usage locations of types are not possible.
- The system guarantees that types always remain consistent by hindering unwanted delete operations.
- If a type is deleted, all usage locations in the user program are deleted.

Properties

By using types you can make the changes centrally and update them in the complete project.

Figure 3-32: Typifying with user libraries

- Types are always marked in the project for better identification

3.7.3 Differences for typifiable objects for controller and HMI

There are system-related differences between the typifiable objects for controllers and HMI:

Table 3-8: Differences of types for controller and HMI

<table>
<thead>
<tr>
<th>Controller</th>
<th>HMI</th>
</tr>
</thead>
<tbody>
<tr>
<td>Subordinate control elements are typified.</td>
<td>Subordinate HMI elements are not typified.</td>
</tr>
<tr>
<td>Subordinate control elements are instanced.</td>
<td>Subordinate HMI elements are not instanced.</td>
</tr>
<tr>
<td>Control elements are edited in a test environment.</td>
<td>HMI images and HMI scripts are edited in a test environment. Faceplates and HMI - UDTs are directly edited in the library without test environment.</td>
</tr>
</tbody>
</table>
Further information on the handling of libraries can be found in the following example.

### 3.7.4 Versioning of a block

**Example: Creating a type**

The following example shows you how the basic functions of the libraries are used with types.

**Table 3-9: Creating a type**

<table>
<thead>
<tr>
<th>Step</th>
<th>Instruction</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>Create a new PLC data type with “Add new data type” and create some tags. Later on this is the subordinate type.</td>
</tr>
<tr>
<td>2.</td>
<td>Create a new function block with “Add new Block”. This is the higher-level type.</td>
</tr>
<tr>
<td>3.</td>
<td>Define an input tag of the data type you have created. The PLC data type is therefore subordinate to the function block.</td>
</tr>
<tr>
<td>4.</td>
<td>Drag the function block via drag &amp; drop into the “Types” folder in the project library.</td>
</tr>
</tbody>
</table>
3 General Programming

3.7 Libraries

---

### Step | Instruction
---|---
5. | Optionally assign: Type name, version, author and comment and confirm the dialog with "OK".

![Image of Add Type dialog]

6. | The subordinate PLC data type is automatically also stored in the library.

![Image of Library view]

---

**Example: Changing a type**

Table 3-10: Changing a type

---

### Step | Instruction
---|---
1. | Right-click the block in the "Project library" and select "Edit type"

![Image of Project library with Edit type option]
### 3 General Programming

#### 3.7 Libraries

<table>
<thead>
<tr>
<th>Step</th>
<th>Instruction</th>
</tr>
</thead>
</table>
| 2.   | Select which controller is to be used as test environment and confirm the dialog with “OK”.  

![Image of selecting test environment](image1.png)  

If several controllers in the project use the selected block, a controller has to be selected as test environment. |
| 3.   | The library view opens. A new version of the block has been created and is now marked with “in test”.  

![Image of library view](image2.png) |
| 4.   | Add another input tag.  

![Image of adding input tag](image3.png)  

In this place you have the option to test the change on the block by loading the project onto a controller. When you have finished testing the block, continue with the following steps. |
### 3.7 Libraries

<table>
<thead>
<tr>
<th>Step</th>
<th>Instruction</th>
</tr>
</thead>
<tbody>
<tr>
<td>5.</td>
<td>Click the “Release version” button.</td>
</tr>
<tr>
<td>6.</td>
<td>A dialog box opens. Here you can store a version-related comment. Confirm the dialog with “OK”. If there are several usage locations of the block in different controllers of the project, you can update them all at the same time: “Update instances in the project”. If older versions of the element are no longer required you can delete them by clicking “Delete unused type versions from library”</td>
</tr>
<tr>
<td>7.</td>
<td>Close the library view with “Close library view”</td>
</tr>
</tbody>
</table>
3.8 Process interrupts

The processing of the user program can be influenced by events such as process interrupts. When you need a fast response of the controller to hardware events (e.g. a rising edge of a channel of a digital input module), configure a process interrupt. For each process interrupt a separate OB can be programmed. This OB is called by the operating system of the controller in the event of a process interrupt. The cycle of the controller is therefore interrupted and continued after processing the process interrupt.

Figure 3-33: Process interrupt is calling OB

![Diagram of process interrupt](image)

In the following figure you can see the configuration of a “hardware interrupt” in the hardware configuration of a digital input module.

Figure 3-34: Configuring hardware interrupt

![Configuration of hardware interrupt](image)

**Advantages**

- Fast system response to events (rising, falling edge, etc.)
- Each event can start a separate OB.
3 General Programming

3.8 Process interrupts

**Recommendation**

- Use the process interrupts in order to program fast responses to hardware events.
- If the system responses are not fast enough despite programming a process interrupt, you can still accelerate the responses. Set as small an "Input delay" as possible in the module. A response to an event can always only occur if the input delay has lapsed. The input delay is used for filtering the input signal in order to, for example, compensate faults such as contact bounce or chatter.

Figure 3-35: Setting input delay
3.9 Other performance recommendations

Here you can find some general recommendations that enable faster program processing of the controller.

**Recommendation**

Note the following recommendations for programming S7-1200/1500 controllers in order to achieve a high performance:

- LAD/FBD: Disable “generate ENO” for blocks. This avoids tests at runtime.
- STL: Do not use registers since address and data registers are only emulated for compatibility reasons by S7-1500.

**Note**

You will find further information in the following entry:

How do you disable the ENO enable output of an instruction?

3 General Programming

3.10 SCL programming language: Tips and tricks

3.10.1 Using call templates

Many instructions of the programming languages offer a call template with a list of existing formal parameters.

Example

Table 3-11: Easy expanding of the call template

<table>
<thead>
<tr>
<th>Step</th>
<th>Instruction</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>Drag an instruction from the library into the SCL program. The editor shows the complete call template.</td>
</tr>
<tr>
<td>2.</td>
<td>Now fill in the required parameter and finish the entry with the “Return” button.</td>
</tr>
<tr>
<td>3.</td>
<td>The editor automatically reduces the call template.</td>
</tr>
<tr>
<td>4.</td>
<td>If you want to edit the complete call later on again, proceed as follows. Click into the call at any place and then click “CTRL+SHIFT+SPACE”. You are now in the Call Template mode. The editor expands the call again. You can navigate with the “TAB” button through the parameters.</td>
</tr>
<tr>
<td>5.</td>
<td>Note: In the “Call Template” mode the writing is in italics.</td>
</tr>
</tbody>
</table>
3.10.2 What instruction parameters are mandatory?

If you are expanding the call template, the color coding will show you straight away what formal parameters of an instruction are optional and which ones are not. Mandatory parameters are marked dark.

3.10.3 Drag & drop with entire tag names

In the SCL editor you can also use drag & drop functions. For tag names you are additionally supported. If you want to replace one tag for another, proceed as follows.

Table 3-12: Drag & drop with tags in SCL

<table>
<thead>
<tr>
<th>Step</th>
<th>Instruction</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>Drag the tag via drag &amp; drop to the tag in the program that is to be replaced. Hold the tag for more than 1 second before releasing it.</td>
</tr>
</tbody>
</table>

> hold for 1 second

The complete tag is replaced.
3.10 SCL programming language: Tips and tricks

3.10.4 Efficiently inserting CASE instruction

With the CASE instruction in SCL, it will be exactly jumped to the selected CASE block condition. After executing the CASE block the instruction is finished. This allows you, for example, to check frequently required value ranges more specifically and easily.

Example

```scl
CASE #myVar OF
    5:
        FC5(#myParam);
    10,12:
        FC10(#myParam);
    15:
        FC15(#myParam);
    0..20:
        FCGlobal(#myParam);
// FCGlobal is never called for the values 5, 10, 12 or 15!
ELSE
    END_CASE;
```

3.10.5 No manipulation of loop counters for FOR loop

FOR loops in SCL are pure counter loops, i.e. the number of iterations is fixed when the loop is entered. In a FOR loop, the loop counter cannot be changed. With the EXIT instruction a loop can be interrupted at any time.

Advantages

- The compiler can optimize the program better, since it does not know the number of iterations.

Example

```scl
FOR #var := #lower TO #upper DO
    #var := #var + 1; // no effect, Compiler -> Warning
END_FOR;
```
3.10 SCL programming language: Tips and tricks

3.10.6 FOR loop backwards

In SCL you can also increment the index of FOR loops backwards or in another step width. For this, use the optional “BY” key word in the loop head.

Example

FOR #var := #upper TO #lower BY -2 DO

END_FOR;

If you are defining “BY” as “-2”, as in the example, the counter is lowered by 2 in every iteration. If you omit “BY”, the default setting for “BY” is 1

3.10.7 Simple creating of instances for calls

If you prefer to work with the keyboard, there is a simple possibility to create instances for blocks in SCL.

Example

Table 3-13: Easy creation of instances

<table>
<thead>
<tr>
<th>Step</th>
<th>Instruction</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>Give the block name a: followed by a &quot;.&quot; (dot). The automatic compilation now shows you the following.</td>
</tr>
<tr>
<td>2.</td>
<td>On the top you can see the already existing instances. In addition, you can directly create a new single instance or multi-instance. Use the shortcuts &quot;s&quot; or &quot;m&quot; to go directly to the respective entries in the automatic compilation window.</td>
</tr>
</tbody>
</table>

3.10.8 Handling of time tags

You can calculate the time tags in SCL just as with normal numbers i.e. you do not need to look for additional functions, such as, e.g. T_COMBINE but you can use simple arithmetic. This approach is called “overload of operands”. The SCL compiler automatically uses the suitable functions. You can use a reasonable arithmetic for the time types and can therefore program more efficiently.

Example

TimeDifference := TimeStamp_1 – TimeStamp_2;
The following table summarizes the overloaded operators and which operation is behind it:

### Table 3-14: Overloaded operands for SCL

<table>
<thead>
<tr>
<th>Overloaded operand</th>
<th>Operation</th>
</tr>
</thead>
<tbody>
<tr>
<td>ltime + time</td>
<td>T_ADD LTime</td>
</tr>
<tr>
<td>ltime + time</td>
<td>T_SUB LTime</td>
</tr>
<tr>
<td>ltime + lint</td>
<td>T_ADD LTime</td>
</tr>
<tr>
<td>ltime + lint</td>
<td>T_SUB LTime</td>
</tr>
<tr>
<td>time + time</td>
<td>T_ADD Time</td>
</tr>
<tr>
<td>time + time</td>
<td>T_SUB Time</td>
</tr>
<tr>
<td>time + dint</td>
<td>T_ADD Time</td>
</tr>
<tr>
<td>time + dint</td>
<td>T_SUB Time</td>
</tr>
<tr>
<td>ldt + ltime</td>
<td>T_ADD LDT / LTime</td>
</tr>
<tr>
<td>ldt + ltime</td>
<td>T_ADD LDT / LTime</td>
</tr>
<tr>
<td>ldt + time</td>
<td>T_SUB LDT / Time</td>
</tr>
<tr>
<td>ldt + time</td>
<td>T_SUB LDT / Time</td>
</tr>
<tr>
<td>dtl + ltime</td>
<td>T_ADD DTL / LTime</td>
</tr>
<tr>
<td>dtl + ltime</td>
<td>T_ADD DTL / LTime</td>
</tr>
<tr>
<td>dtl + time</td>
<td>T_SUB DTL / Time</td>
</tr>
<tr>
<td>dtl + time</td>
<td>T_SUB DTL / Time</td>
</tr>
<tr>
<td>ltod + ltime</td>
<td>T_ADD LTOD / LTime</td>
</tr>
<tr>
<td>ltod + ltime</td>
<td>T_ADD LTOD / LTime</td>
</tr>
<tr>
<td>ltod + dint</td>
<td>T_SUB LTOD / LTime</td>
</tr>
<tr>
<td>ltod + dint</td>
<td>T_SUB LTOD / LTime</td>
</tr>
<tr>
<td>tod + time</td>
<td>T_ADD TOD / Time</td>
</tr>
<tr>
<td>tod + time</td>
<td>T_SUB TOD / Time</td>
</tr>
<tr>
<td>tod + dint</td>
<td>T_ADD TOD / Time</td>
</tr>
<tr>
<td>tod + dint</td>
<td>T_SUB TOD / Time</td>
</tr>
<tr>
<td>dt + time</td>
<td>T_ADD DT / Time</td>
</tr>
<tr>
<td>dt + time</td>
<td>T_SUB DT / Time</td>
</tr>
<tr>
<td>ldt – ldt</td>
<td>T_DIFF LDT</td>
</tr>
<tr>
<td>dtl – dtl</td>
<td>T_DIFF DTL</td>
</tr>
<tr>
<td>dt – dt</td>
<td>T_DIFF DT</td>
</tr>
<tr>
<td>date – date</td>
<td>T_DIFF DATE</td>
</tr>
<tr>
<td>ltod – ltod</td>
<td>T_DIFF LTOD</td>
</tr>
<tr>
<td>date + ltod</td>
<td>T_COMBINE DATE / LTOD</td>
</tr>
<tr>
<td>date + tod</td>
<td>T_COMBINE DATE / TOD</td>
</tr>
</tbody>
</table>
4 Hardware-Independent Programming

4.1 Data types of S7-300/400 and S7-1200/1500

To make sure that a block can be used on all controllers without any further adjustments, it is important not use hardware-dependent functions and properties.

Below is a list of all elementary data types and data groups.

Recommendation

- Only use the data types that are supported by the controllers on which the program is to run.

<table>
<thead>
<tr>
<th>Description</th>
<th>S7 - 300/400</th>
<th>S7-1200</th>
<th>S7-1500</th>
</tr>
</thead>
<tbody>
<tr>
<td>Bit data types</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>• BOOL</td>
<td>✔</td>
<td>✔</td>
<td>✔</td>
</tr>
<tr>
<td>• BYTE</td>
<td>✔</td>
<td>✔</td>
<td>✔</td>
</tr>
<tr>
<td>• WORD</td>
<td>✔</td>
<td>✔</td>
<td>✔</td>
</tr>
<tr>
<td>• DWORD</td>
<td>✔</td>
<td>✔</td>
<td>✔</td>
</tr>
<tr>
<td>• LWORD</td>
<td>✗</td>
<td>✗</td>
<td>✔</td>
</tr>
<tr>
<td>Character type</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>• CHAR (8 bit)</td>
<td>✔</td>
<td>✔</td>
<td>✔</td>
</tr>
<tr>
<td>Numerical data types</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>• INT (16 bit)</td>
<td>✔</td>
<td>✔</td>
<td>✔</td>
</tr>
<tr>
<td>• DINT (32 bit)</td>
<td>✔</td>
<td>✔</td>
<td>✔</td>
</tr>
<tr>
<td>• REAL (32 bit)</td>
<td>✔</td>
<td>✔</td>
<td>✔</td>
</tr>
<tr>
<td>• SINT (8 bit)</td>
<td>✔</td>
<td>✔</td>
<td>✔</td>
</tr>
<tr>
<td>• USINT (8 bit)</td>
<td>✔</td>
<td>✔</td>
<td>✔</td>
</tr>
<tr>
<td>• UINT (16 bit)</td>
<td>✔</td>
<td>✔</td>
<td>✔</td>
</tr>
<tr>
<td>• UDINT (32 bit)</td>
<td>✔</td>
<td>✔</td>
<td>✔</td>
</tr>
<tr>
<td>• LREAL (64 bit)</td>
<td>✔</td>
<td>✔</td>
<td>✔</td>
</tr>
<tr>
<td>• LINT (64 bit)</td>
<td>✗</td>
<td>✗</td>
<td>✔</td>
</tr>
<tr>
<td>• ULINT (64 bit)</td>
<td>✗</td>
<td>✗</td>
<td>✔</td>
</tr>
<tr>
<td>Time types</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>• TIME</td>
<td>✔</td>
<td>✔</td>
<td>✔</td>
</tr>
<tr>
<td>• DATE</td>
<td>✔</td>
<td>✔</td>
<td>✔</td>
</tr>
<tr>
<td>• TIME_OF_DAY</td>
<td>✔</td>
<td>✔</td>
<td>✔</td>
</tr>
<tr>
<td>• S5TIME</td>
<td>✔</td>
<td>✗</td>
<td>✔</td>
</tr>
<tr>
<td>• LTIME</td>
<td>✗</td>
<td>✗</td>
<td>✔</td>
</tr>
<tr>
<td>• L_TIME_OF_DAY</td>
<td>✗</td>
<td>✗</td>
<td>✔</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Description</th>
<th>S7 - 300/400</th>
<th>S7-1200</th>
<th>S7-1500</th>
</tr>
</thead>
<tbody>
<tr>
<td>Time types</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>• DT (DATE_AND_TIME)</td>
<td>✔</td>
<td>✗</td>
<td>✔</td>
</tr>
<tr>
<td>• DTL</td>
<td>✗</td>
<td>✔</td>
<td>✔</td>
</tr>
</tbody>
</table>
4 Hardware-Independent Programming

4.2 No bit memory but global data blocks

### Table 4-3: Parameter types for formal parameters that are transferred between blocks

<table>
<thead>
<tr>
<th>Description</th>
<th>S7 - 300/400</th>
<th>S7-1200</th>
<th>S7-1500</th>
</tr>
</thead>
<tbody>
<tr>
<td>Pointer</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>• POINTER</td>
<td>✓</td>
<td>x</td>
<td>✓ ¹</td>
</tr>
<tr>
<td>• ANY</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>• VARIANT</td>
<td>x</td>
<td>✓</td>
<td></td>
</tr>
<tr>
<td>Blocks</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>• TIMER</td>
<td>✓</td>
<td></td>
<td></td>
</tr>
<tr>
<td>• COUNTER</td>
<td>✓</td>
<td></td>
<td></td>
</tr>
<tr>
<td>• BLOCK_FB</td>
<td>✓</td>
<td>x</td>
<td></td>
</tr>
<tr>
<td>• BLOCK_FC</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>• BLOCK_DB</td>
<td>✓</td>
<td>x</td>
<td></td>
</tr>
<tr>
<td>• BLOCK_SDB</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>• VOID</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
</tr>
<tr>
<td>PLC data types</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>• PLC Data Type</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
</tr>
</tbody>
</table>

¹) For S7-1500 the ARRAY data type is limited to 64 bit instead of 16 bit

²) For S7-1200/1500 the TIMER and COUNTER data type is represented by IEC_TIMER and IEC_Counter.

### Advantages

- Optimized global DBs are clearly more powerful than the bit memory address area that is not optimized for reasons of compatibility.

### Recommendation

- The handling with bit memory is problematic, since every controller has a bit memory address area with a different size. Do not use bit memory for the programming but always global data blocks. This is how the program can always be used universally.
4.3 Programming of "clock bits"

Recommendation

For the programming of clock memory bits, the hardware configuration always has to be correct.

Use a programmed block as clock generator. Below, you can find a programming example for a clock generator in the SCL programming language.

Example

The programmed block has the following functions. A desired frequency is specified. The “Q” output is a Boolean value that toggles in the desired frequency. The “Countdown” output outputs the remaining time of the current state of “Q”.

If the desired frequency is smaller or equal 0.0, then the output Q = FALSE and Countdown = 0.0.

Note

The complete programming example is available for free download in the following entry:

5 The Most Important Recommendations

- Use optimized blocks
  - Chapter 2.6 Optimized blocks
- Structuring the program clearly and well
  - Chapter 3.2 Organization blocks (OB)
- Inserting instructions as multi-instance (TON, TOF ..)
  - Chapter 3.2.5 Multi-instances
- Reusable programming of blocks
  - Chapter 3.2.8 Reusability of blocks
- Symbolic programming
  - Chapter 3.6 Symbolic addressing
- When handling data, work with ARRAY
  - Chapter 3.6.2 ARRAY data type and indirect field accesses
- Creating PLC data types
  - Chapter 3.6.3 STRUCT data type and PLC data types
- Using libraries for storing program elements
  - Chapter 3.7 Libraries
- No memory bits but global data blocks
  - Chapter 4.2 No bit memory but global data blocks
# 6 Related Literature

<table>
<thead>
<tr>
<th>Topic</th>
<th>Title</th>
</tr>
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<tbody>
<tr>
<td>1\ Siemens Industry Online Support</td>
<td><a href="http://support.automation.siemens.com">http://support.automation.siemens.com</a></td>
</tr>
</tbody>
</table>
7 History

Table 7-1

<table>
<thead>
<tr>
<th>Version</th>
<th>Date</th>
<th>Modifications</th>
</tr>
</thead>
<tbody>
<tr>
<td>V1.0</td>
<td>09/2013</td>
<td>First version</td>
</tr>
<tr>
<td>V1.1</td>
<td>10/2013</td>
<td>Corrections in the following chapters:</td>
</tr>
<tr>
<td></td>
<td></td>
<td>2.6.3 Best possible data storage in the processor on S7-1500</td>
</tr>
<tr>
<td></td>
<td></td>
<td>2.12 Internal reference ID for controller and HMI tags</td>
</tr>
<tr>
<td></td>
<td></td>
<td>3.2.2 Functions (FC)</td>
</tr>
<tr>
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<td></td>
<td>3.2.3 Function blocks (FB)</td>
</tr>
<tr>
<td></td>
<td></td>
<td>3.4.3 Local memory</td>
</tr>
<tr>
<td>V1.2</td>
<td>03/2014</td>
<td>New chapter:</td>
</tr>
<tr>
<td></td>
<td></td>
<td>2.6.4 Conversion between optimized and non-optimized tags</td>
</tr>
<tr>
<td></td>
<td></td>
<td>2.6.5 Communication with optimized data</td>
</tr>
<tr>
<td></td>
<td></td>
<td>2.9.2 MOVE instructions</td>
</tr>
<tr>
<td></td>
<td></td>
<td>2.9.3 RUNTIME</td>
</tr>
<tr>
<td></td>
<td></td>
<td>3.6.4 Access to I/O areas with PLC data types</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Extension of following chapter:</td>
</tr>
<tr>
<td></td>
<td></td>
<td>2.2 Terms</td>
</tr>
<tr>
<td></td>
<td></td>
<td>2.3 Programming languages</td>
</tr>
<tr>
<td></td>
<td></td>
<td>2.6 Optimized blocks</td>
</tr>
<tr>
<td></td>
<td></td>
<td>2.8.2 Date_Time_Long data type</td>
</tr>
<tr>
<td></td>
<td></td>
<td>2.10 Symbolic and comments</td>
</tr>
<tr>
<td></td>
<td></td>
<td>3.2 Program blocks</td>
</tr>
<tr>
<td></td>
<td></td>
<td>3.5 Retentivity</td>
</tr>
<tr>
<td></td>
<td></td>
<td>4.3 Programming of &quot;clock bits&quot;</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Several corrections in different chapter</td>
</tr>
</tbody>
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